Hidden pits of SiC MOSFET benchmarking and evaluation Aleš Loidl, *STMicroelectronics*

Sic

Bodo's Wide Bandgap Event 2024 Making WBG Designs Happen

ST silicon carbide business

Business Development

~85 customers, ~130 programs awarded
\$1.14B revenues in 2023 (+60% vs. 2022)
\$5B+ revenue opportunity by 2030
>40% market share in SiC MOSFETs and power modules
Source: Omdia

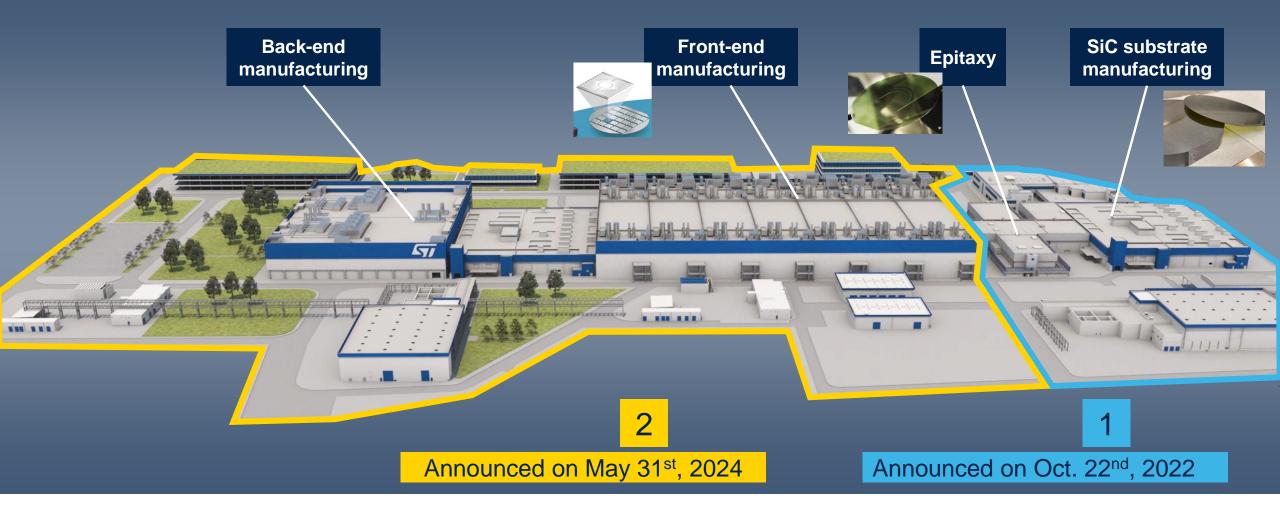
Portfolio of SiC MOSFETs & diodes

- 650V to 1700V SiC MOSFET product range
- 3rd generation MOSFETs in high-volume production
- Flexible approach covering discrete packages, modules, dice

Portfolio & technology



ST silicon carbide campus in Catania Vertically integrated silicon carbide facility

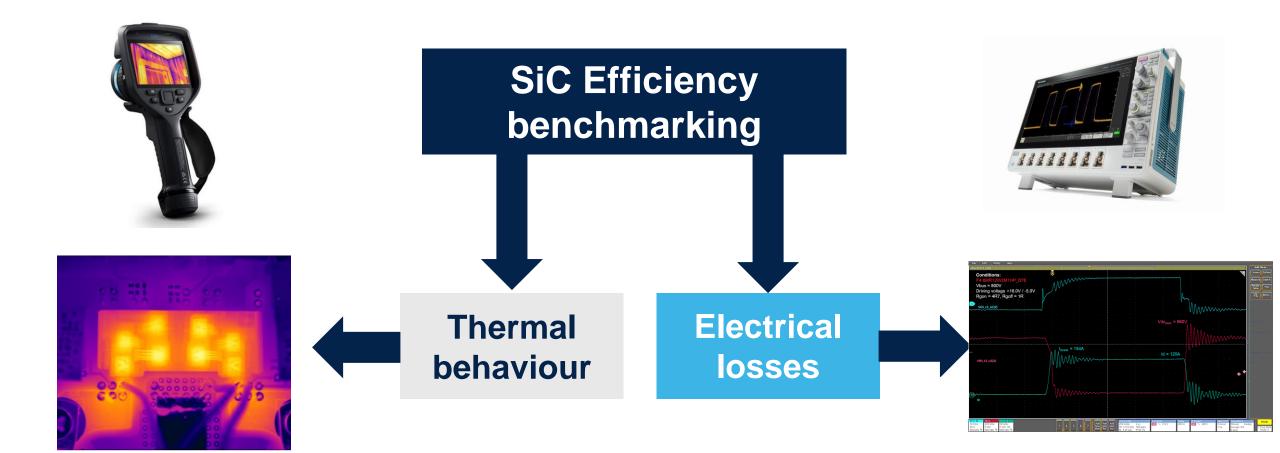




SiC MOSFET Benchmarking

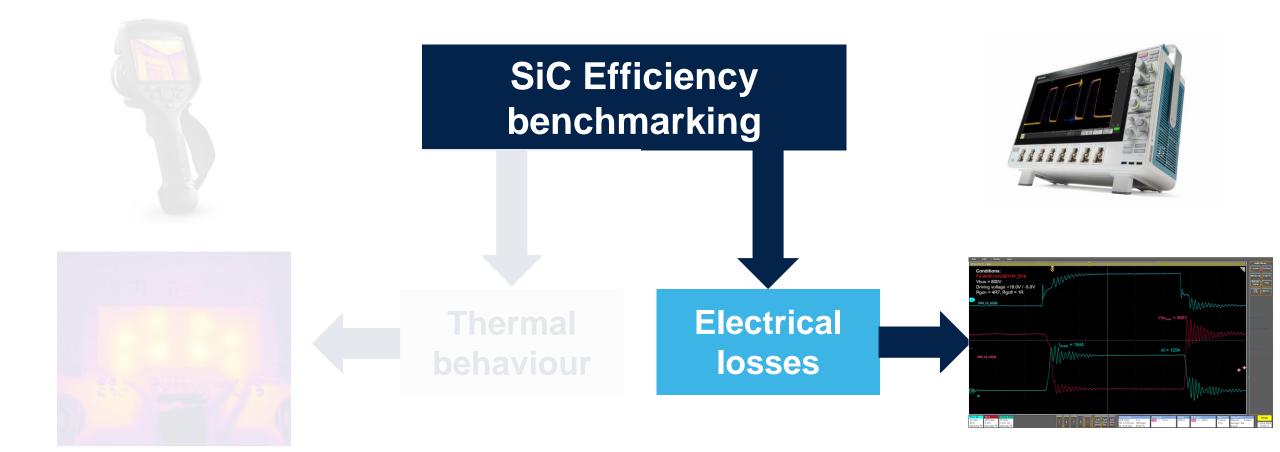


Efficiency Benchmarking of the devices





Efficiency Benchmarking of the devices

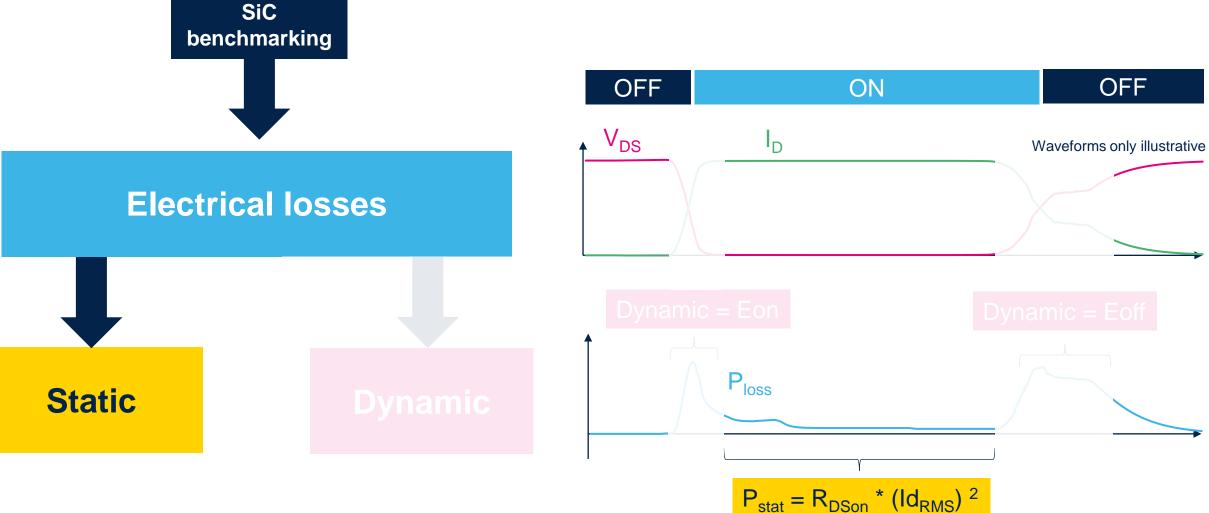




Benchmarking of the devices electrical behavior SiC benchmarking OFF OFF ON V_{DS} I_D Waveforms only illustrative **Electrical losses** P_{loss} **Static Dynamic**



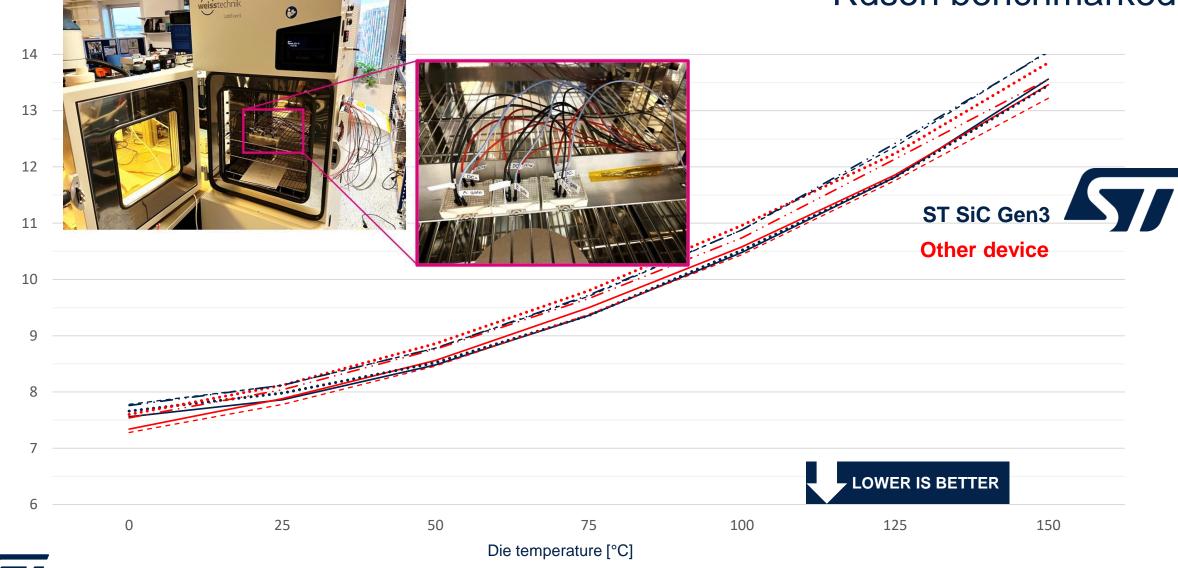
Benchmarking of the devices electrical behavior







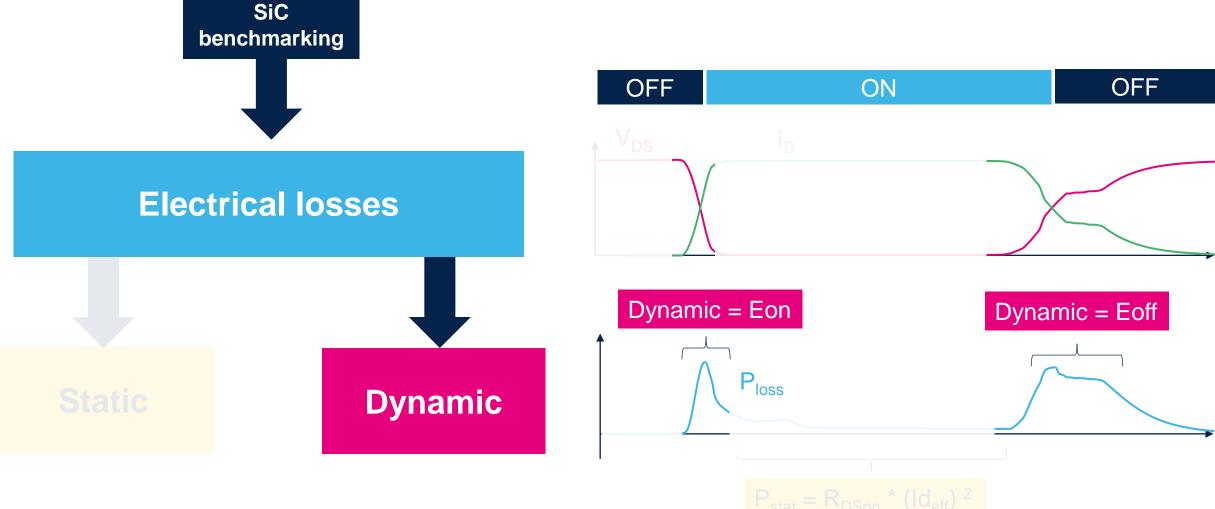
Static losses Rdson benchmarked





 Rds_{ON} [m Ω]

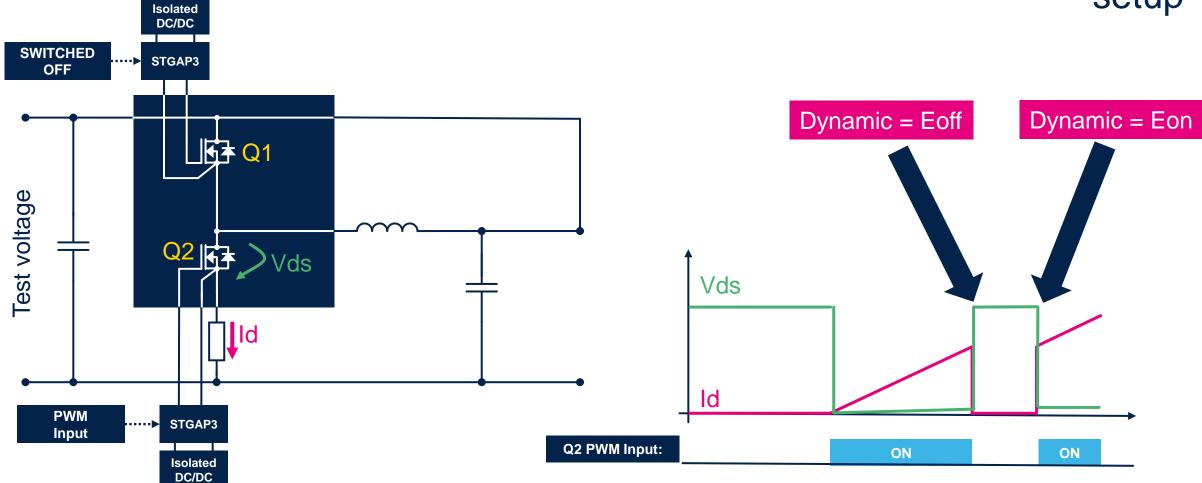
Benchmarking of the devices electrical behavior







Double pulse test setup







Voltage measurement:

- HV passive probe 100:1 250MHz
- HV passive probe 100:1 400MHz
- Differential Probe IsoVu 500V 1GHz
- Differential Probe BumbleBee 1000V 400MHz

Hidden pits Measurement tools

Current measurement:

- Coaxial shunt $50m\Omega$
- Rogowski coil 20MHz

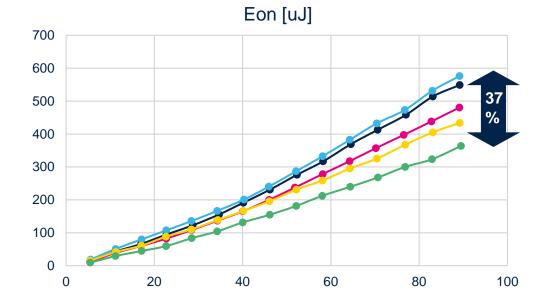
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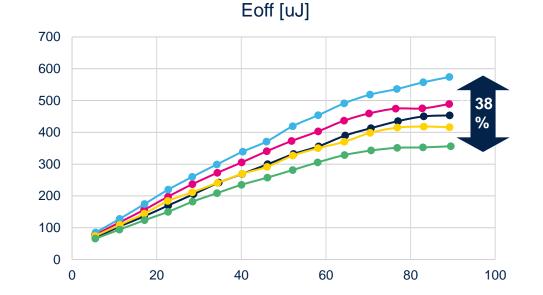






Hidden pits Double pulse testing - Effect of different voltage probes

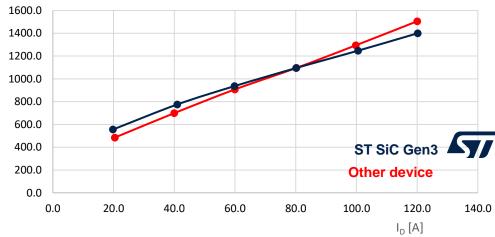




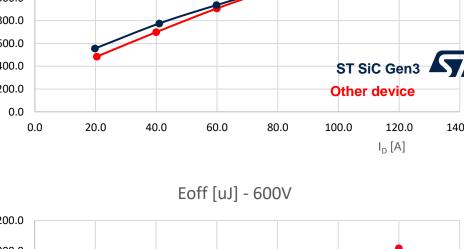
- HV passive probe 100:1 250MHz
- + HV passive probe 100:1 400MHz
- ---- IsoVu 500V 1GHz
- IsoVu 500V 1GHz with offset comp.
- IsoVu 500V 1GHz after new self calib.

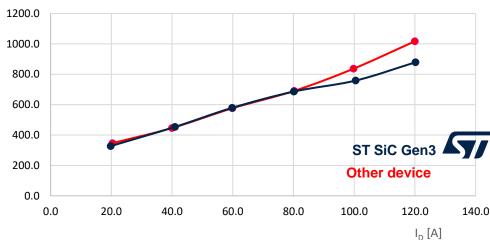


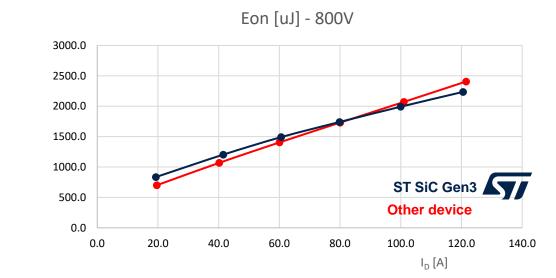
Dynamic Losses Eon – Eoff Comparison/benchmark



Eon [uJ] - 600V

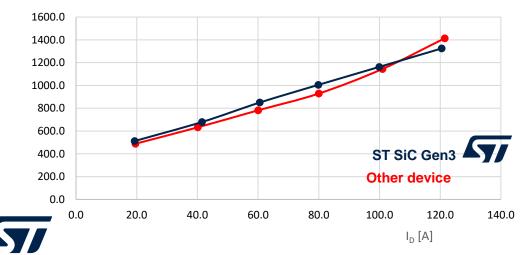






life.augmented

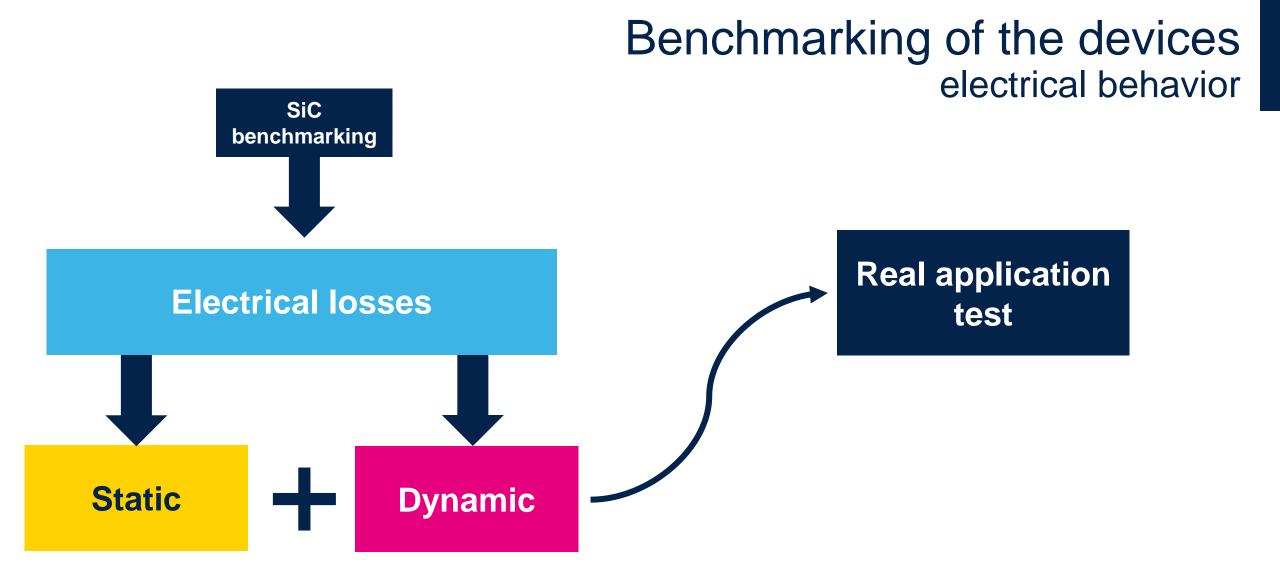






Full application tests



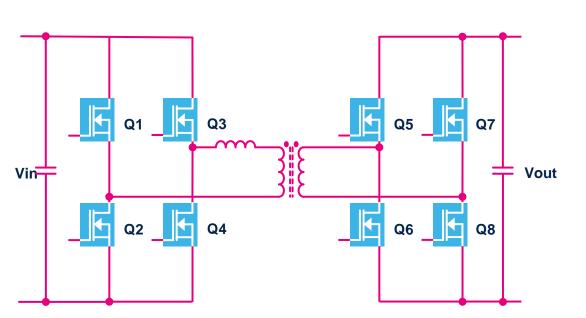




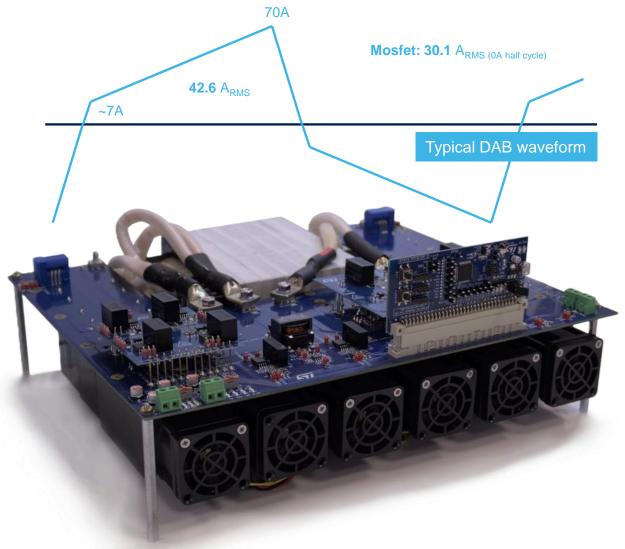


DAB real block scheme

Dual Active Bridge Structure & waveform example

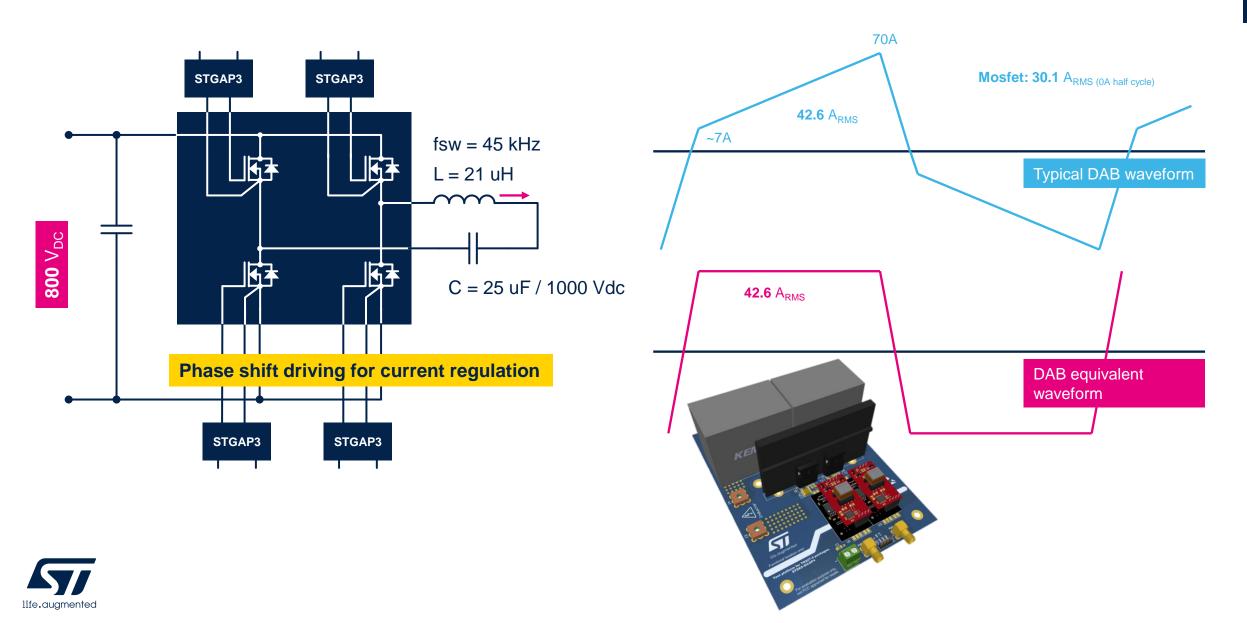


The best, but too complex



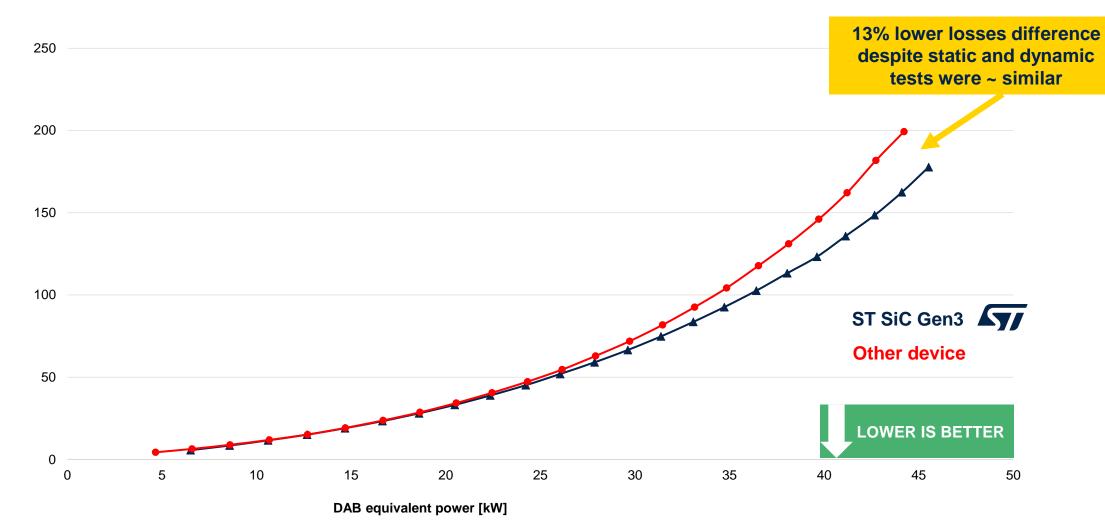


P^{ower}**LAB** Simplified converter with same device's stress





DAB testing method Efficiency Result





Our technology starts with You



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Bonus slide

Hidden pits



Hidden pits Application Measurement example

- Positive oscillations seen by probe A
- Application was running perfectly
- Negative oscillations seen by probe A
 with switched connectors
- Oscillations not seen by probe type B

